

U.S. Pat. App. No.: 09/841,974
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IN THE SPECIFICATION:

On page 4, between lines 23 and 24, please insert:

--Figure 3 schematically shows an emulator for emulating a logic circuit in accordance with various embodiments of the present invention.--

On page 4, between lines 30 and 31, please insert:

--Figure 3 illustrates an emulator 300, which includes a circuit partitioner 301, a configurer 302, and a plurality of programmable logic devices 303. The circuit partitioner 301 synthesizes a first circuit partition and a second circuit partition from a user circuit. The configurer then configures (a) the first circuit partition in a first programmable logic device 303 and the second circuit partition in a second programmable logic device 303.--